

FIG. 1 is a block diagram of a system 100 for processing signals. The system 100 includes a transmitter 102, a receiver 110, and a front end 108. The transmitter 102 is connected to the front end 108, which is connected to the receiver 110. The front end 108 includes an ADC 106 and a filter 112. The receiver 110 includes a filter 114 and an ADC 106. The system 100 is configured to process signals received by the receiver 110 and transmit the processed signals by the transmitter 102.

100

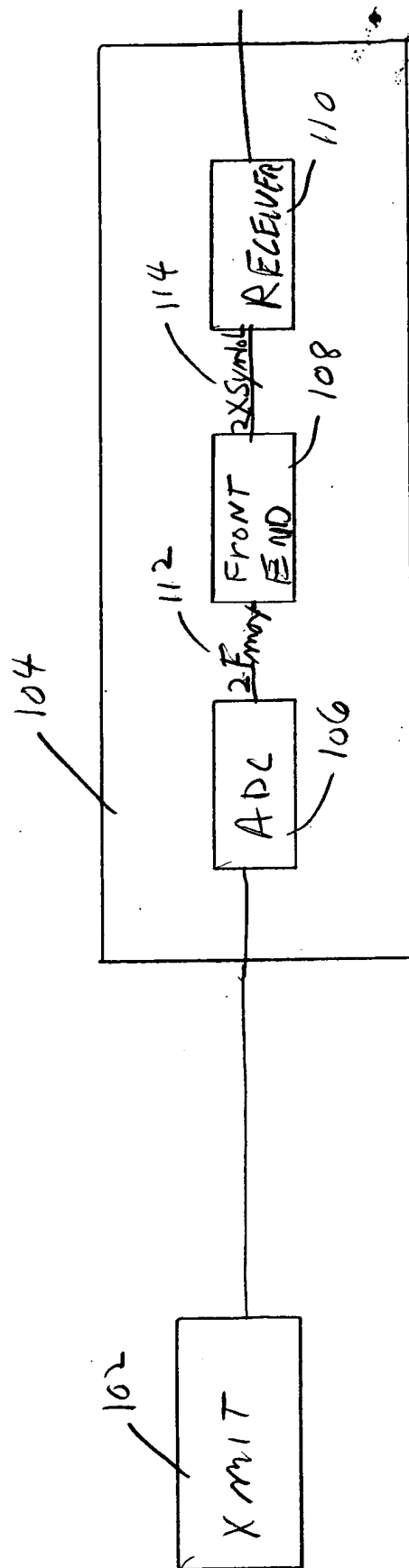


FIGURE 1

FIGURE 2A

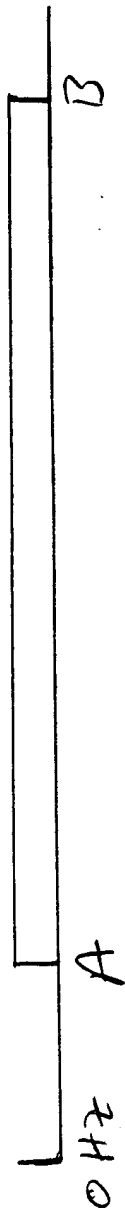


FIGURE 2B

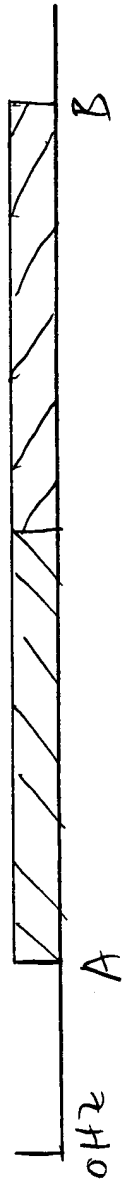


FIGURE 2C

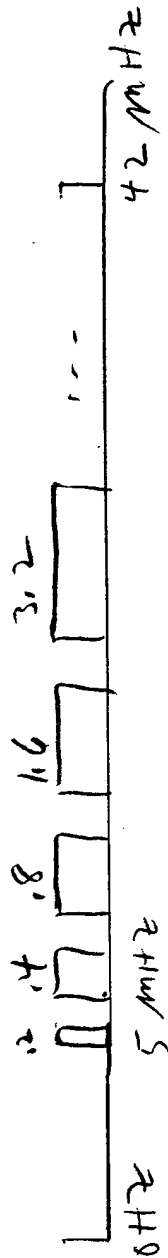


FIGURE 2D

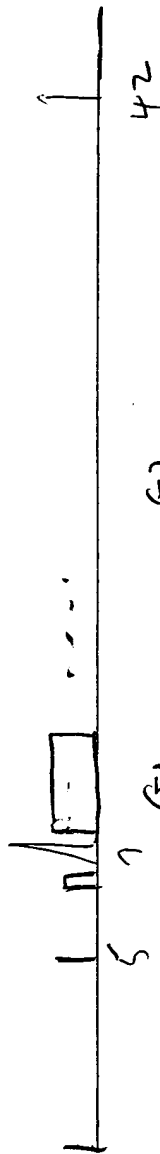


FIGURE 2E



100

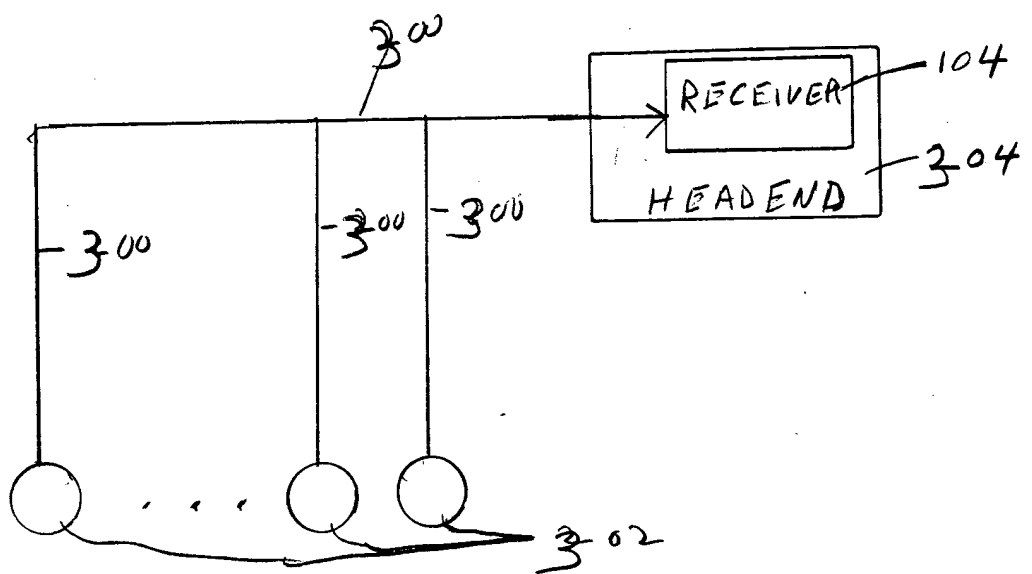


FIGURE 3

FIGURE 4

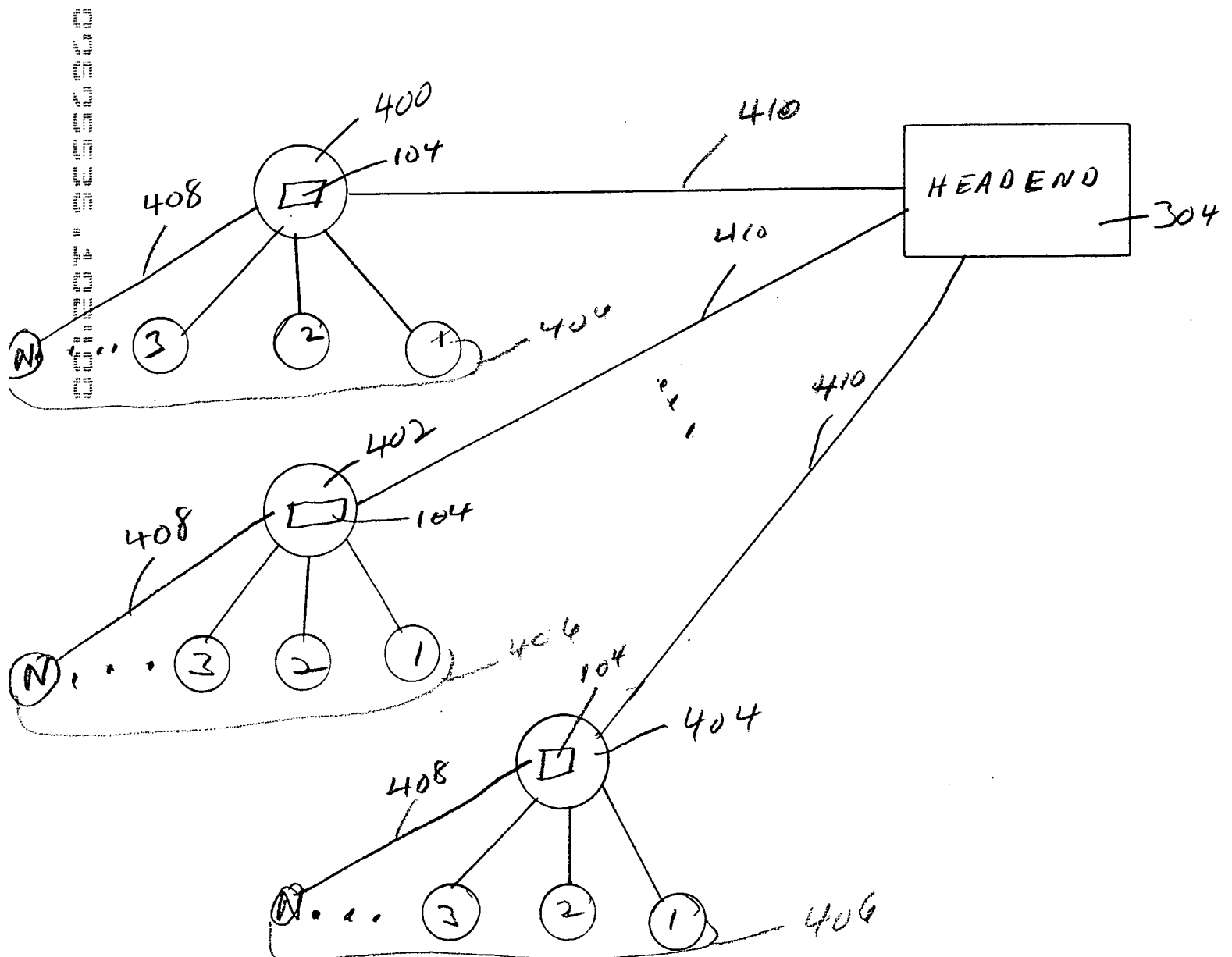


FIG. 5 is a block diagram of a system for processing a signal.

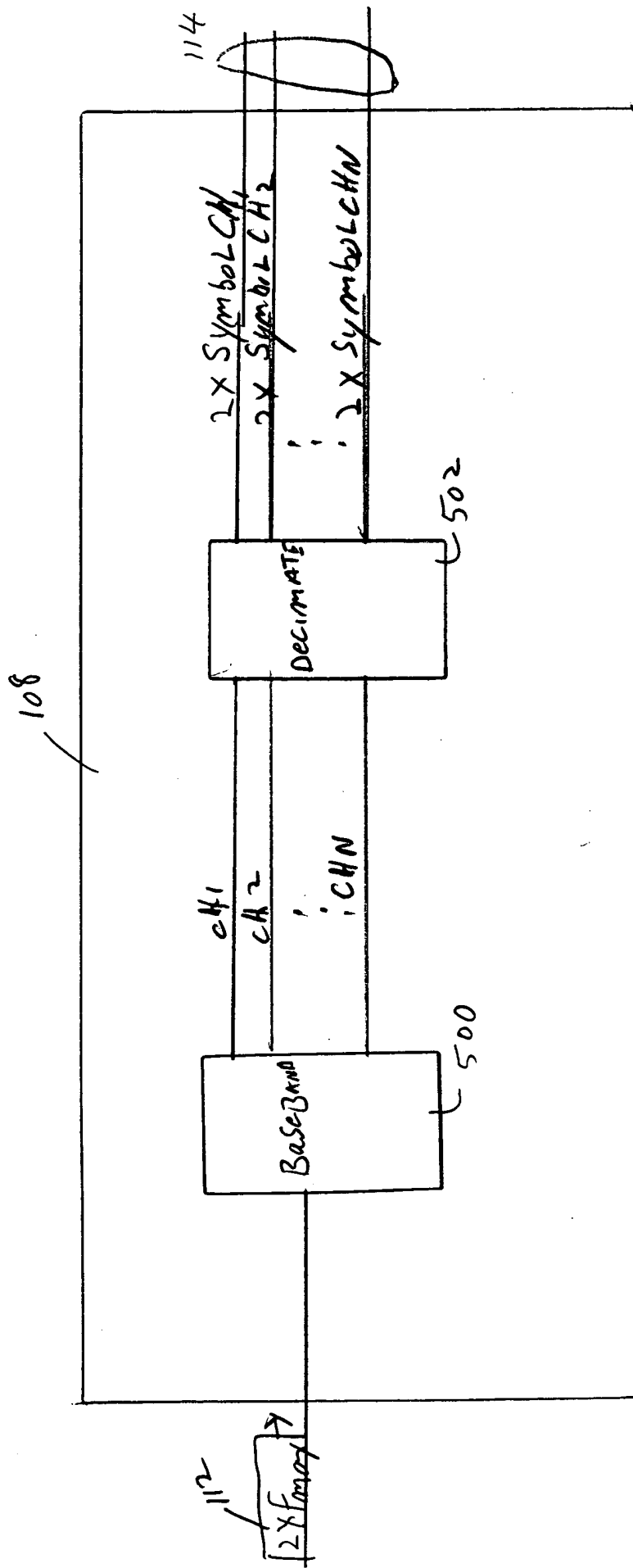


FIGURE 5

FIG. 11

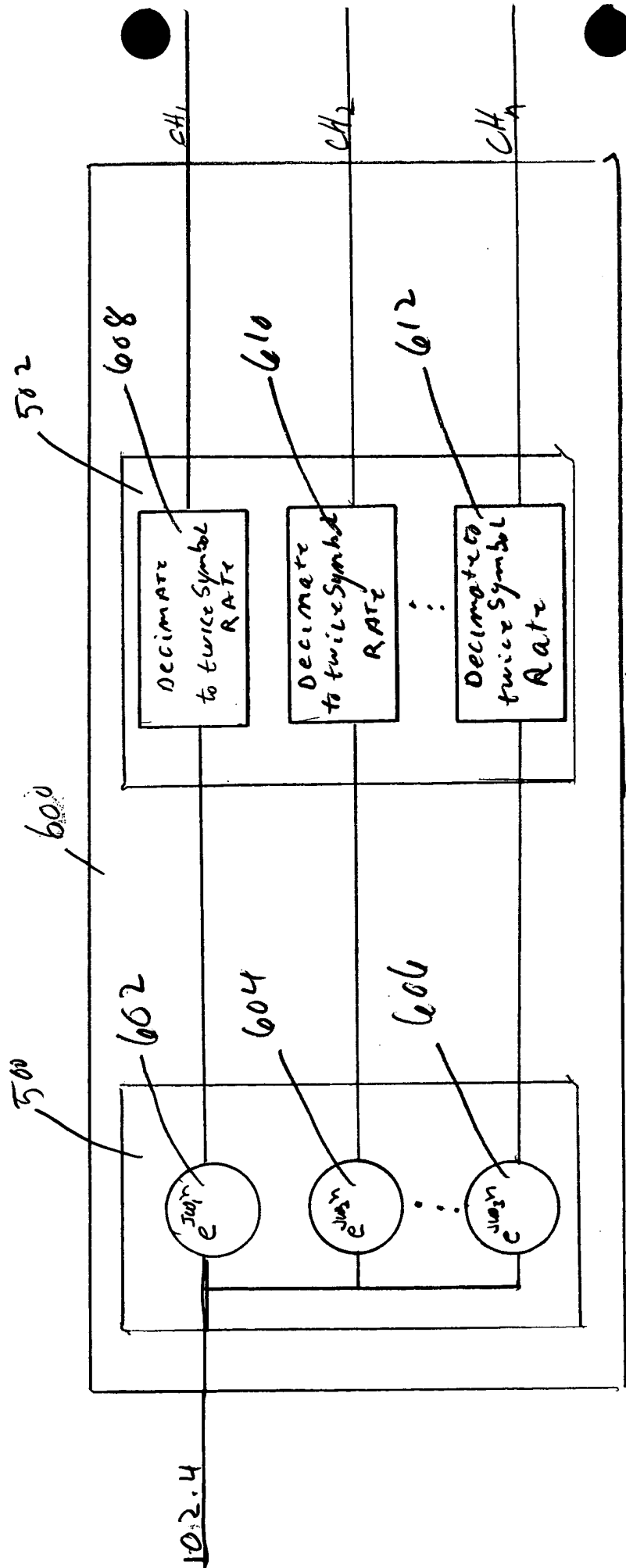
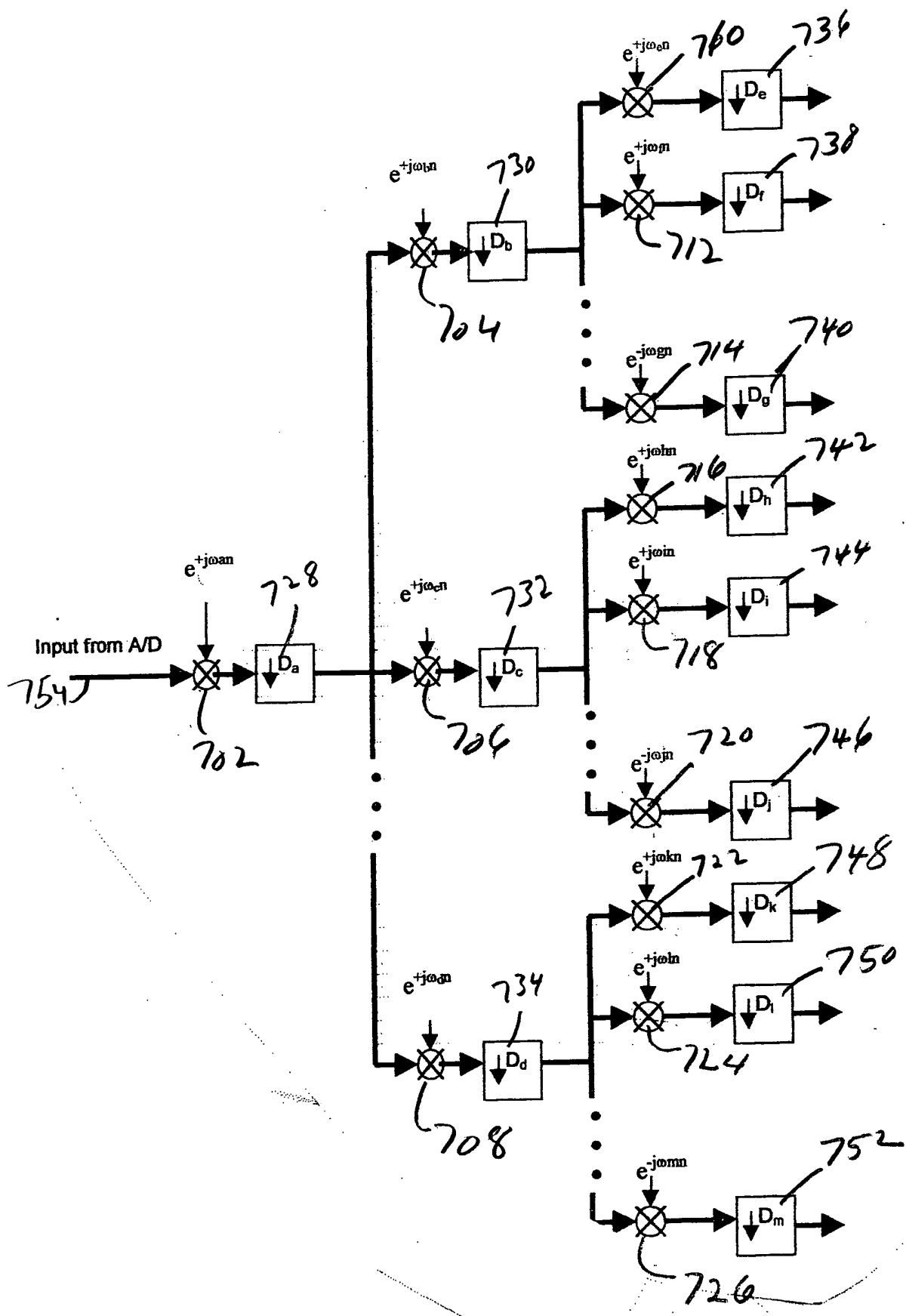


FIGURE 6

FIGURE 7



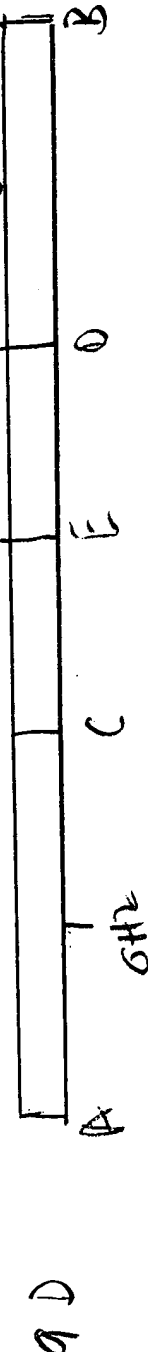
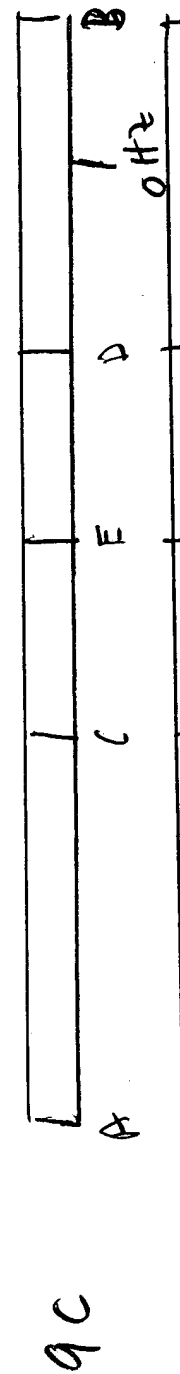
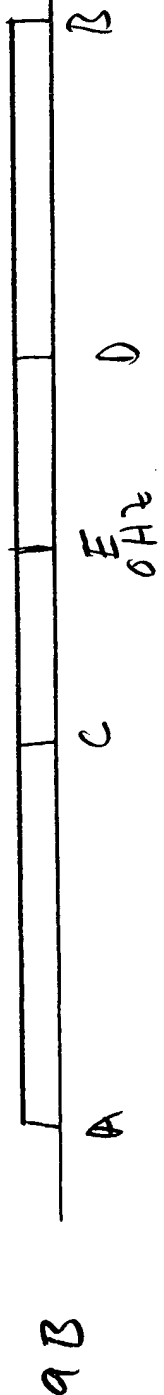
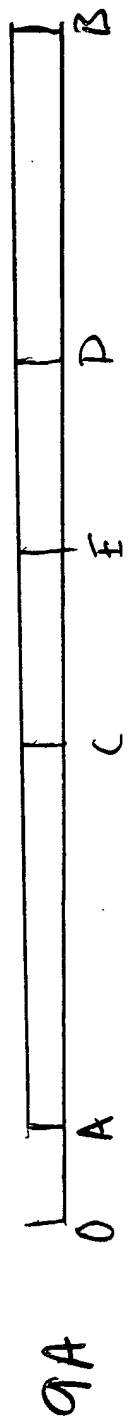
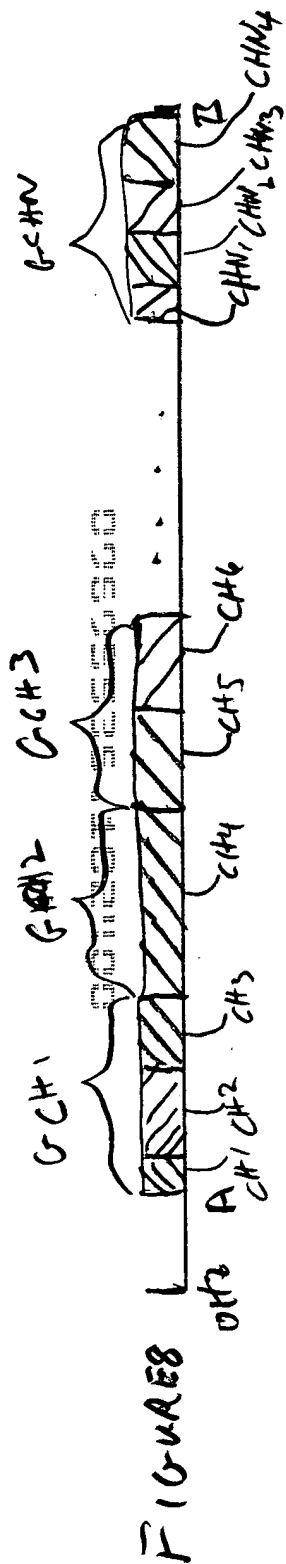


FIGURE 10

1000

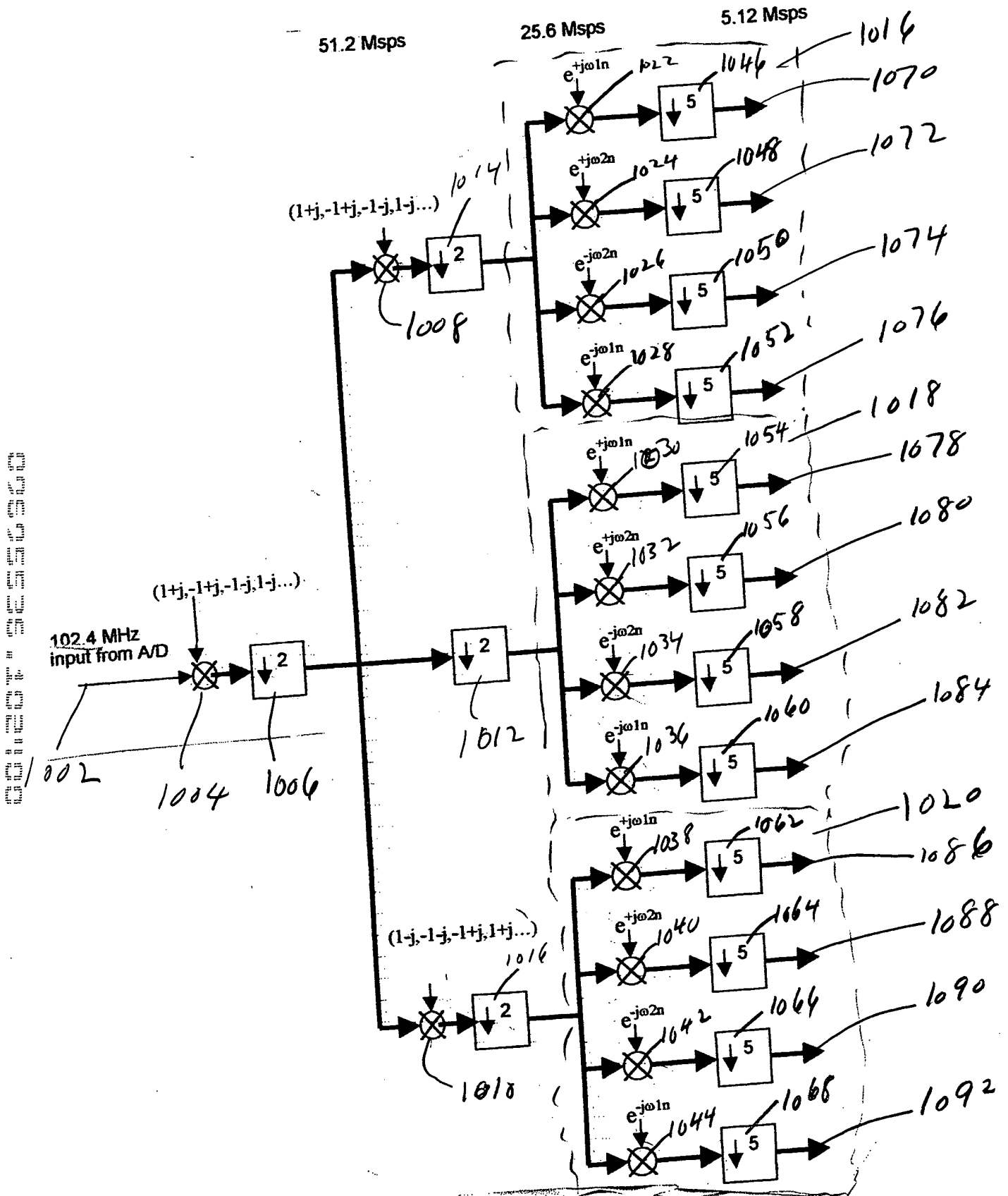


FIGURE 11

1100

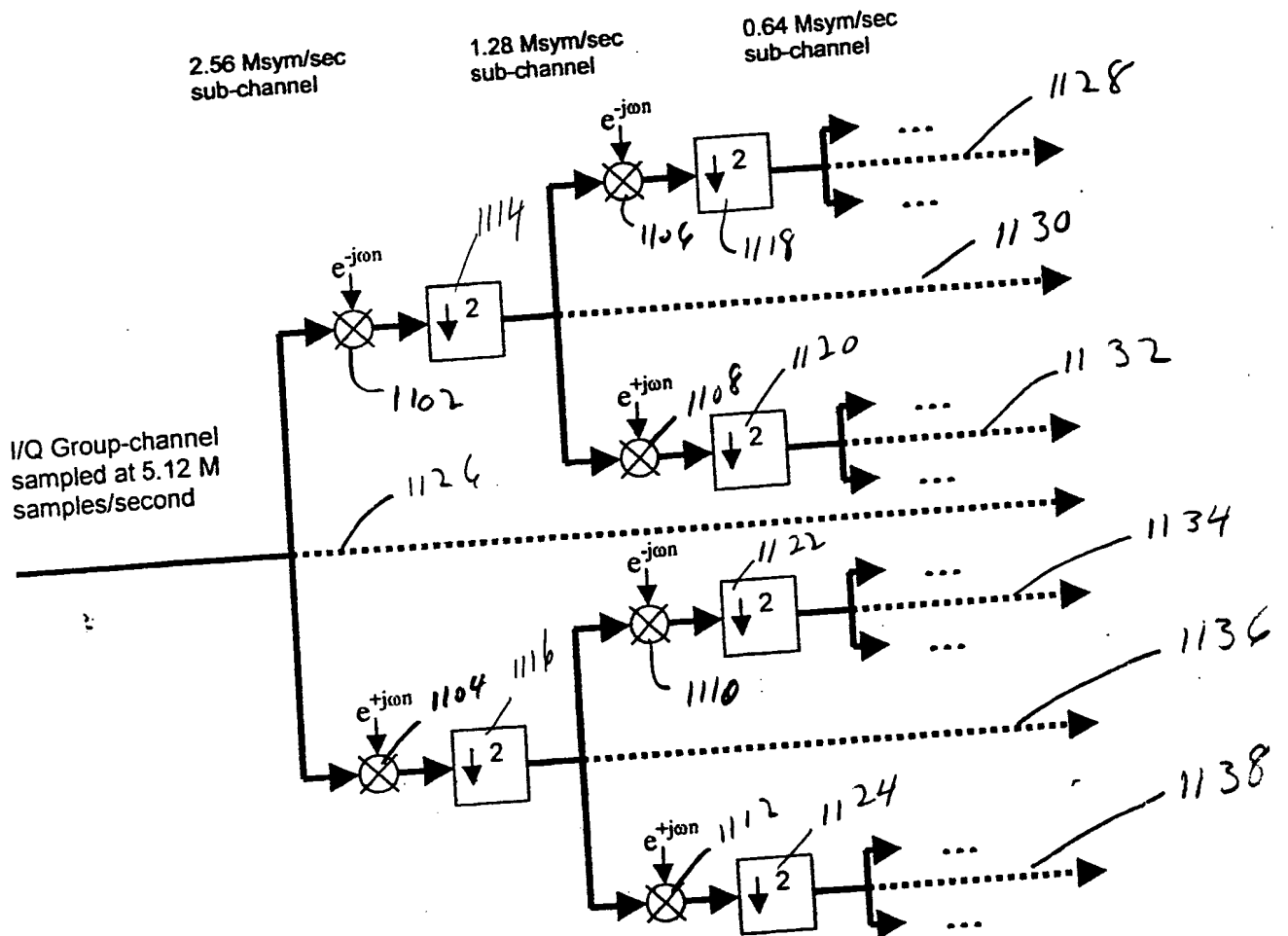
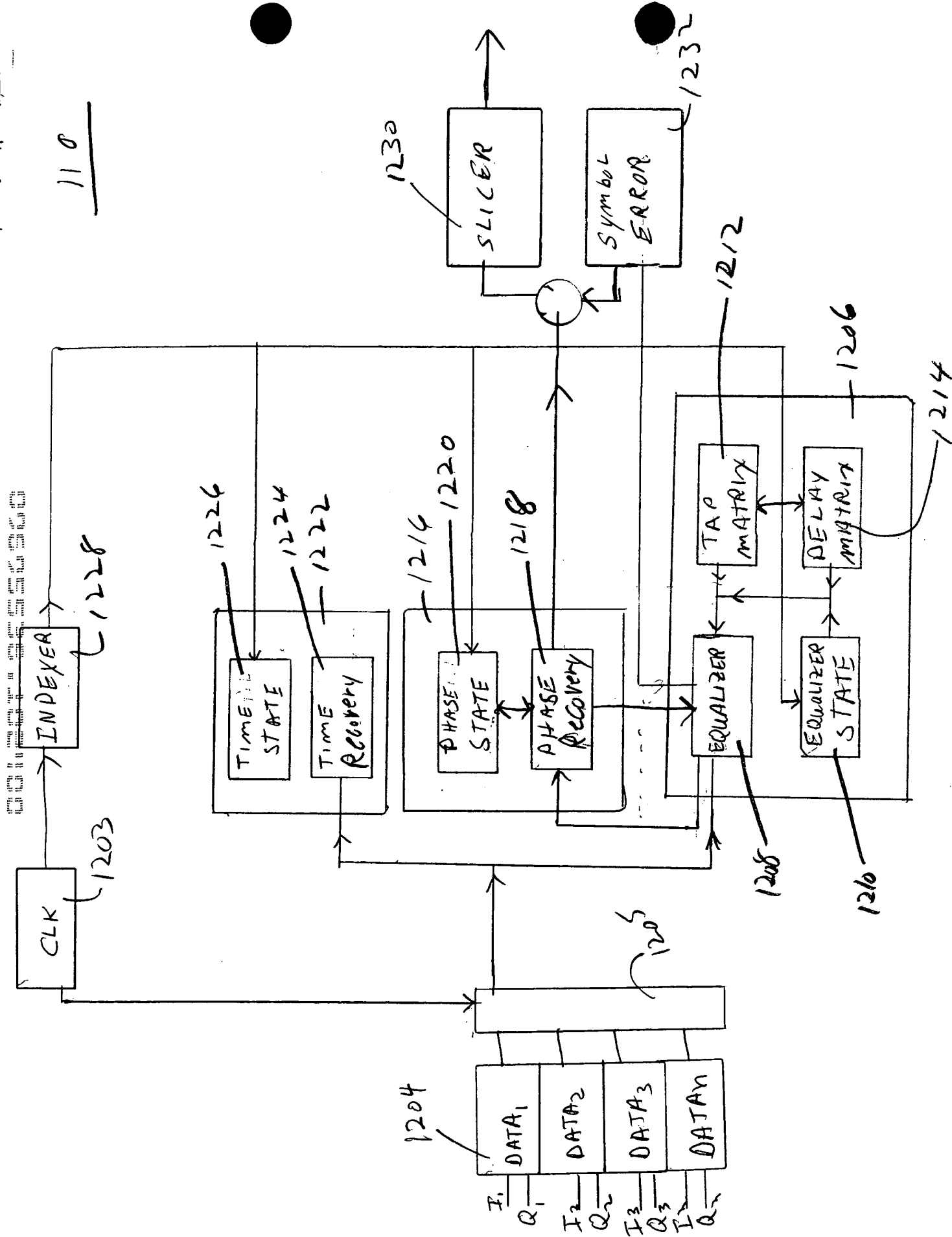


FIGURE 12



110

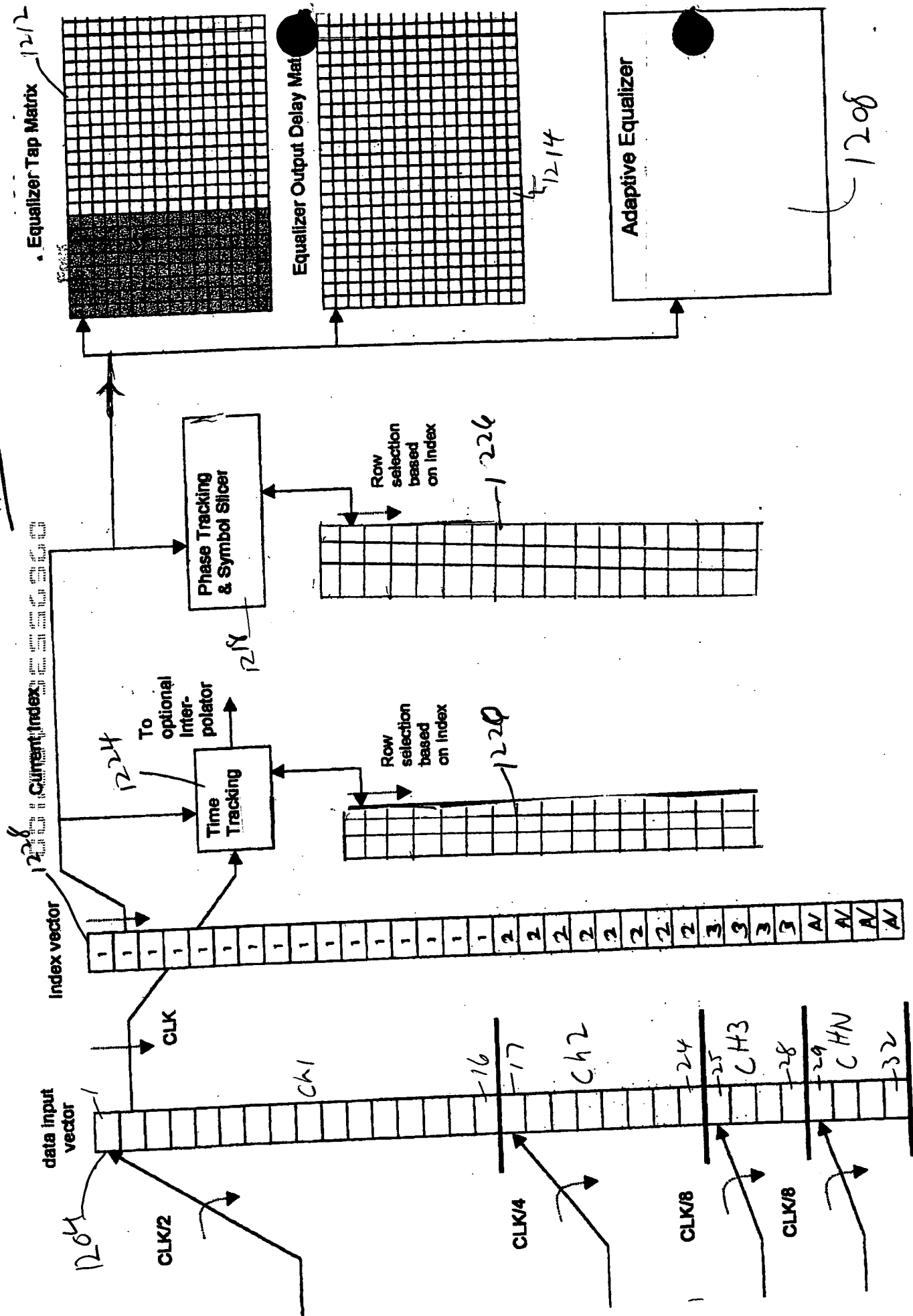


FIGURE 13